



PATENT  
Conf. No. 6539

IN THE UNITED STATES PATENT OFFICE

Applicants: Atul V. Ghia et al.

Assignee: Xilinx, Inc.

Title: "Low Jitter Clock for a Physical Media Access  
Sublayer on a Field Programmable Gate Array"

Serial No.: 10/090,239 File Date: 03-01-02

Examiner: Anh Q. Tran Art Unit: 2819

Docket No.: X-1061 US Conf. No.: 6539

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Respectfully submitted,



Kim Kanzaki  
Attorney for Applicants  
Reg. No. 37,652

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